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He received the B.S., M.S., and Dr. Eng. degrees from Hokkaido University, Sapporo, Japan, in 1979, 1981, and 1986, respectively. Since 1983 he has been with Hokkaido University. He is now Professor at Division of Information Communication Systems in Graduate School of Information Science and Technology, Hokkaido University. He was the dean of Graduate School of Information Science and Technology (2014-2018). He is also the director of Global Station for Big-Data and Cybersecurity (GSB), GI-CoRE, Hokkaido University (2018-present). From 1984 to 1985, he was a visiting researcher at Department of Computer Science, University of Illinois, USA.

His research interests are in the areas of speech signal processing, wireless communication signal processing and low-power consumption VLSI system design. He has published 3 books, over 180 Transaction/Journal papers, and more than 300 International Conference/Symposium/Workshop papers.

Dr. Miyanaga served as an associate editor of IEICE Transactions on Fundamentals of Electronics, Communications and Computer Science from 1996 to 1999, editors of IEICE Transactions on Fundamentals, Special Issues. He was an associate editor of IEEE CAS Society Transaction on Circuits and Systems II (2012-2014), an associate editor of Journal of Signal Processing, RISP Japan (2005-2014) and also the Editor-in-Chief, Journal of Signal Processing, RISP Japan (2015-2018).

He was a delegate of IEICE, Engineering Sciences Society Steering Committee, i.e., IEICE ESS Officers from 2004 to 2006. He was a chair of Technical Group on Smart Info-Media System, IEICE (IEICE TG-SIS) during the same period and now a member of the advisory committee, IEICE TG-SIS. He was vice-President, IEICE Engineering Science (ES) Society (2010-2011), President-elect (2014) and President (2015). He was the Editor-in-Chief, IEICE ESS (2016-2018). He is now an auditor of IEICE. He is Fellow member of IEICE.

He served as a member in the board of directors, IEEE Japan Council as a chair of student activity committee from 2002 to 2004. He was a standing committee chair of student activity committee in IEEE Sapporo Section (1998-2006), member development (2007-2012), fellow committee (2013-2014) and vice-Chair, IEEE Sapporo Section (2015-2016). He is currently Chair, IEEE Sapporo Section (2017-present).

He was a secretary of IEEE Circuits and Systems Society, Technical Committee on Digital Signal Processing (IEEE CASS DSP TC) (2004-2006) and was its chair (2006-2008). He was a distinguished lecture (DL) of IEEE CAS Society (2010-2011) and a Board of Governor (BoG) of IEEE CAS Society (2011-2013).